

SEARCHED	INDEXED
SERIALIZED	FILED
ISSUE CLASSIFICATION	

**CPA**

01-1082

UTILITY SERIAL NUMBER	PATENT DATE		PATENT NUMBER	
SERIAL NUMBER 08/897,953	FILING DATE 10/24/97	CLASS 431	SUBCLASS	GROUP ART UNIT EXAMINER

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\*\*CONTINUING DATA\*\*\*\*\*

VERIFIED THIS APPLN IS A CON OF 08/393,677

\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*

VERIFIED JAPAN 6-088762

**CPA**

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
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Verified and Acknowledged Examiner's Initials

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**TITLE**

METHOD AND APPARATUS FOR FABRICATING SEMICONDUCTOR DEVICE

U.S. DEPT. OF COMM./PAT. & TM—PTO-436L (Rev. 12-94)

<b>PARTS OF APPLICATION FILED SEPARATELY</b>		Applications Examiner	
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim
Assistant Examiner			
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		ISSUE BATCH NUMBER	
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Label Area			